

Pablo A. Levi

✉ pablo.levi@gmail.com
☎ +1 408-886-4278
🇺🇸 United States Citizen

Summary

I built the computing power that runs the world. Now I help people harness that power.

Education

MBA <i>MBA degree program with emphasis in technology companies</i>	<i>Technion - Israel Institute for Technology</i> <i>Oct 2009 – Jun 2011</i>
B.Sc. in Computer Science <i>5-year Bachelor's degree program</i>	<i>Universidad Nacional del Sur (Argentina)</i> <i>Mar 1995 – Aug 2000</i>

Areas of Expertise

Computer Science	Algorithms, data structures, systems architecture, CPU/GPU architectures, software architecture and development, design patterns
Agentic Engineering	Development of AI agents, LLM verification, agentic workflows, RAG, fine-tuning, agentic prototyping
CPU/GPU/AI Architecture	Simulation and performance analysis of CPUs, GPUs, and AI processing units
VLSI Verification	VLSI design and verification process from initial requirements through final product, pre-silicon verification, simulation, verification strategies and methodologies, prototyping technologies
Programming languages	C/C++, Python, Perl, Java, Assembler, SystemVerilog, Specman, Javascript/Typescript
Technologies Management	Verification testbenches (UVM, SVTB), Docker, Next.js, React
Business	Estimation and planning tasks, project management, leading new initiatives, coaching, mentoring, people management
	Formulation of corporate strategy, analysis of competitive strategy, industry analysis, communication with different parties at multiple technical levels

Work Experience

Stealth <i>Researcher</i>	<i>January 2024 – Present</i> <i>San Francisco, CA</i>
• Research AI applications and productivity • Develop agentic engineering methodologies and prototyping • Develop LLM verification methodologies	
Quadric.ai <i>Senior Principal Engineer/Verification Lead</i>	<i>Apr 2019 – December 2023</i> <i>Burlingame, CA</i>
• Led the architecture's verification efforts • Completed fully functional first-generation chip • Developed the verification strategy used since first generation chip • Defined and tracked project completion metrics • Created the software architecture used to verify the design • Wrote and maintained simulators and software infrastructure used for functional VLSI verification • Implemented the prototyping technologies used in the company • Coordinated the work of junior engineers and remote engineers across different time zones • Interviewed candidates for engineering roles	

NVIDIA Corp.
Senior GPU Architect

Jan 2016 - Feb 2019
Santa Clara, CA

- Participated in the development of Maxwell, Pascal, Volta, and Turing families of GPUs, serving the whole spectrum of the processing power for the gaming, enterprise, high-performance, automobile, and AI applications
- Designed the architecture, wrote, and maintained the software used for the performance analysis of GPUs by all business units in the company
- Collaborated with multiple internal customer groups to collect feedback on the software
- Mentored team members

NVIDIA Corp.
Senior Verification Engineer/GPU ASIC and DV Lead

Jul 2013 - Jan 2016
Santa Clara, CA

- Participated in the development of Maxwell and Pascal families of GPUs
- Responsible for the definition and analysis of verification methodologies
- Led the verification of four units in the GPU graphic pipeline, including hands-on-work in:
 - Test plan definition and execution
 - Development and maintenance of simulation models
 - Test writing
 - Definition, analysis and reporting of coverage metrics
- Collaborated with external software vendors, to give feedback for their products and for future improvements to their technologies
- Coordinated the work of junior team members
- Mentored team members
- Developed software infrastructure used for design and verification of Graphic Processing Units (GPUs)

NVIDIA Corp.
Senior Verification Engineer

Oct 2012 - Jul 2013
Santa Clara, CA

- Responsible for the definition and analysis of verification methodologies
- Developed new infrastructure for the GPU ASIC verification teams, including simulation and coverage tools, and power/performance analysis tools and methodologies
- Carried out evaluations of external tools, which directly led to cost reduction in the order of millions of dollars

Intel Israel (74) Ltd.
Senior Verification Engineer/Technical Leader

Sep 2000 - Jul 2012
Haifa, Israel

- Participated in projects that individually contributed for above 80% of Intel's revenue for the years 2000-2014: Baniyas, Dothan, Yonah, Merom, Conroe, Woodcrest, and Sandy Bridge
- Led department-wide working groups that defined methodologies for security verification and coverage metrics
- Led the verification of one of the main sections of the CPU design, including hands-on work in:
 - Effort estimation for all tasks involved in the verification process
 - Definition and implementation of verification testing plans
 - Development and maintenance of simulation models
 - Test writing
 - Definition, analysis, and reporting of coverage metrics
 - Debug of pre- and post-silicon failures
- Mentored and managed engineers, from definition of activities, training and coaching, to performance evaluation
- Prepared and delivered departmental training as subject matter expert in verification technologies:
 - Co-instructed the design and verification boot camps, featuring the main techniques and methodologies for design and verification, mandatory for every new employee

- Co-instructed the main course on the verification language used for the development of simulation models
- Co-developed three internal courses, part of new employee's development plan
- Managed verification teams
- Collaborated with multiple groups to drive new initiatives, from evangelization, definition of requirements, task breakdown, planning, implementation, to monitoring of the progress through KPIs

Paris City Hall (Collaboration)

MBA Consultant

Feb-May 2011

Haifa, Israel

- Analysis of customer requirements
- Research of industries in search of companies that match certain criteria
- Creation of summary and presentation of results back to the customer

Soft Skills

- Excellent analytical skills
- Excellent interpersonal communication skills
- Excellent presentation skills
- Analytical and creative in developing solutions to problems
- Cultural and business acumen
- Languages:
 - Spanish - native
 - Hebrew - bilingual
 - English - bilingual
 - French - conversational
 - Italian - conversational
 - German - basic knowledge
 - Portuguese - basic knowledge